

Figure 1 is a schematic diagram of a 156-bit data structure. The structure is a large rectangle divided into several sections. At the top, a 24-bit section contains 'SFL (24)' and 'FFL (192)'. Below this is a large 1144-bit section containing 'UAT', 'SFL', 'FFL', 'BMT', and '(1144)'. At the bottom, a 26-bit section contains 'SFL', 'FFL', 'BMT', and '(26)'. A dashed line labeled 'WDK OFF' is shown at the bottom left. Dimensions are indicated by numbers: 24, 8, 2, 26, 20, 44, 3, 7, 4, 12, 13, 2, 13, 6, 26, and 156.

[illegible]

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